



SECURED PROCESSING UNIT 100

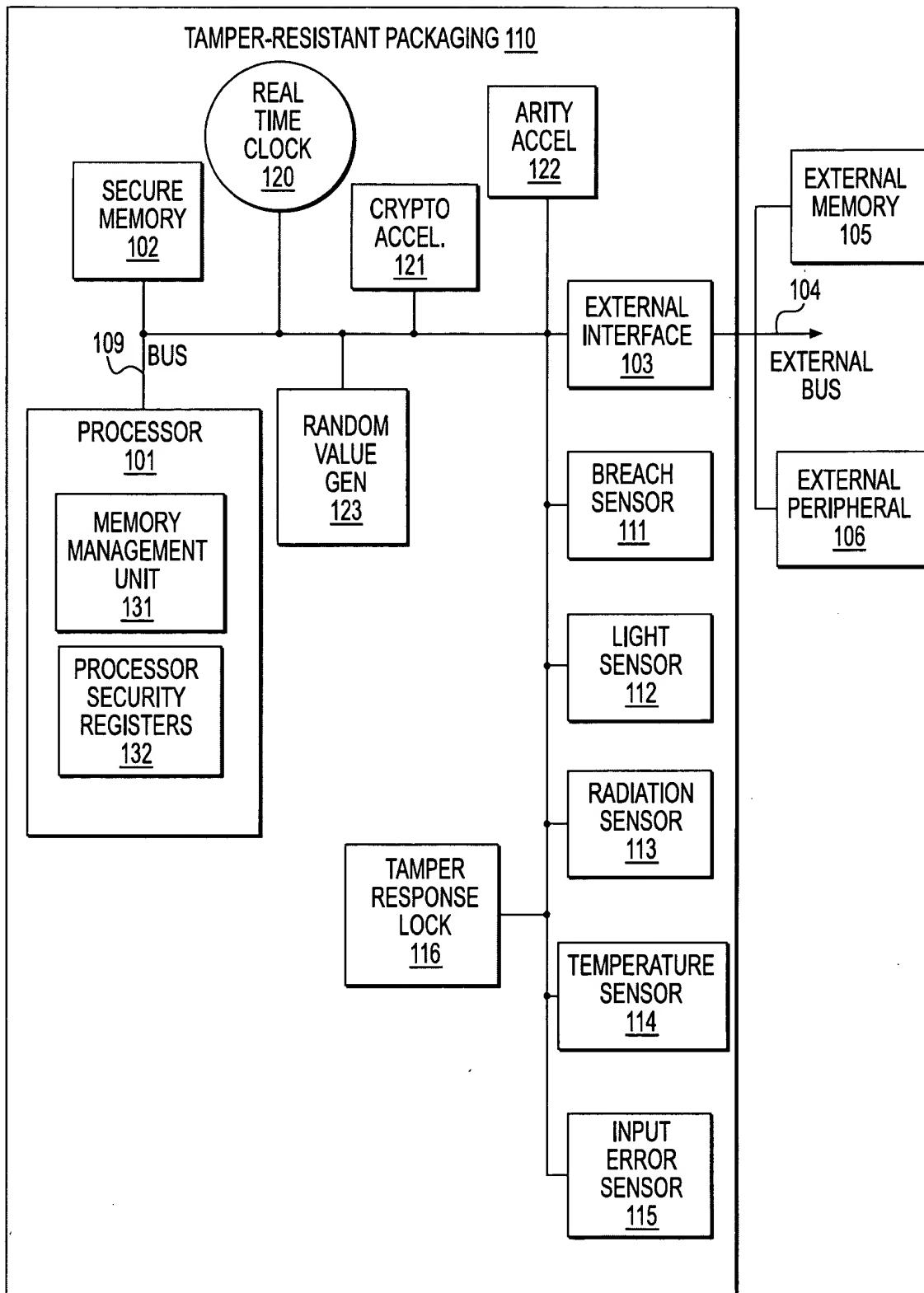


FIG. 1A

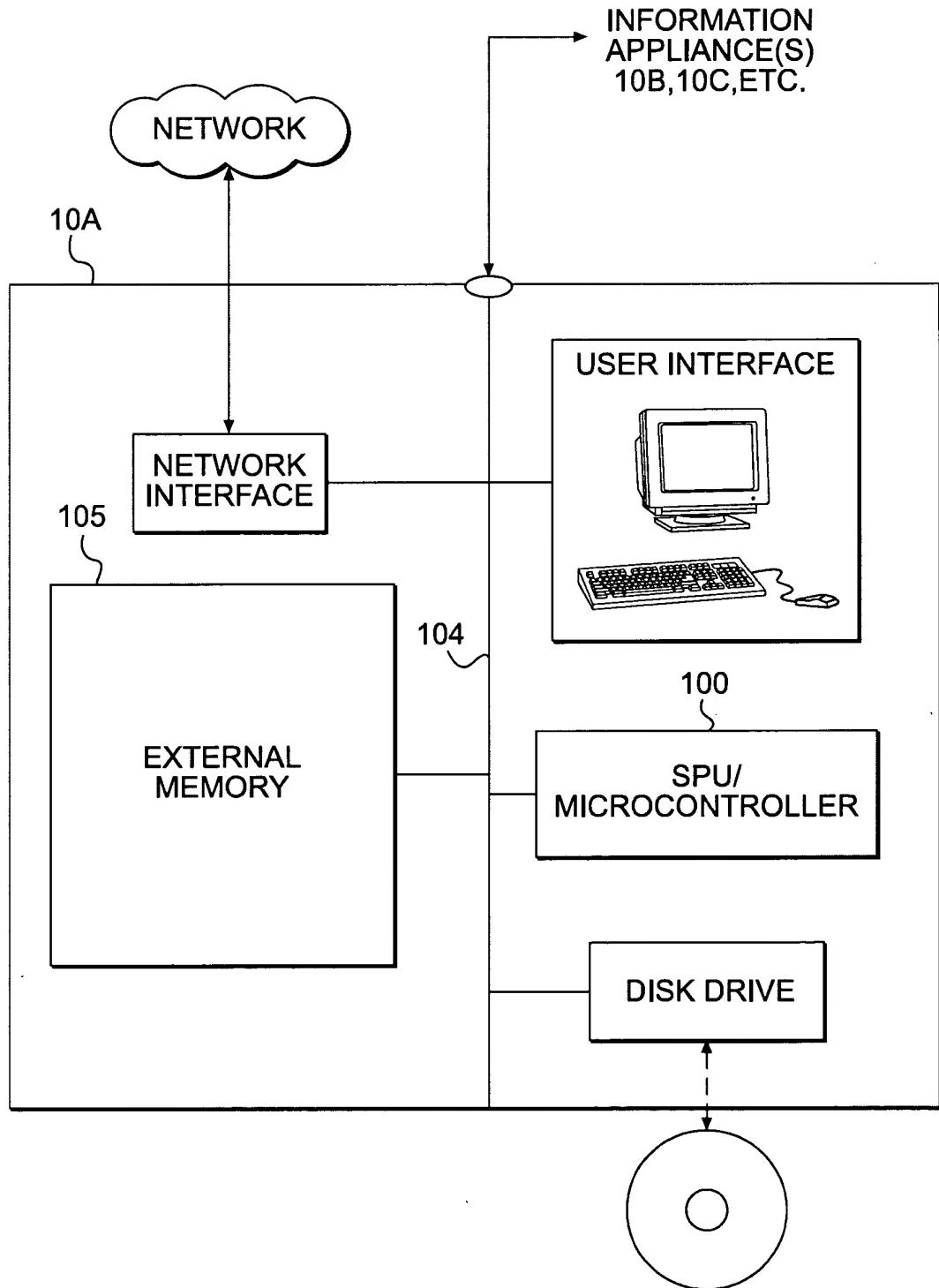


FIG. 1B

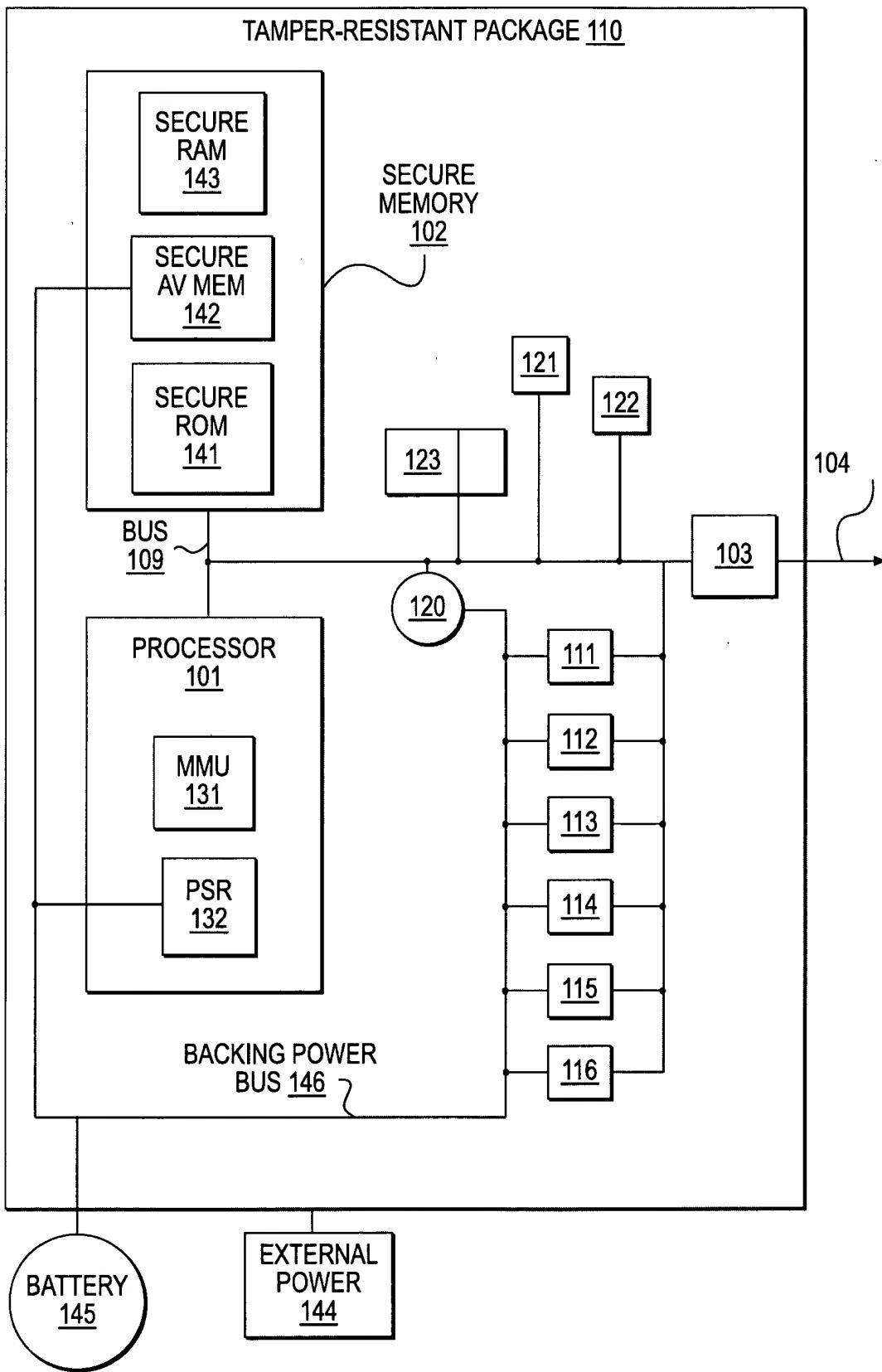


FIG. 2

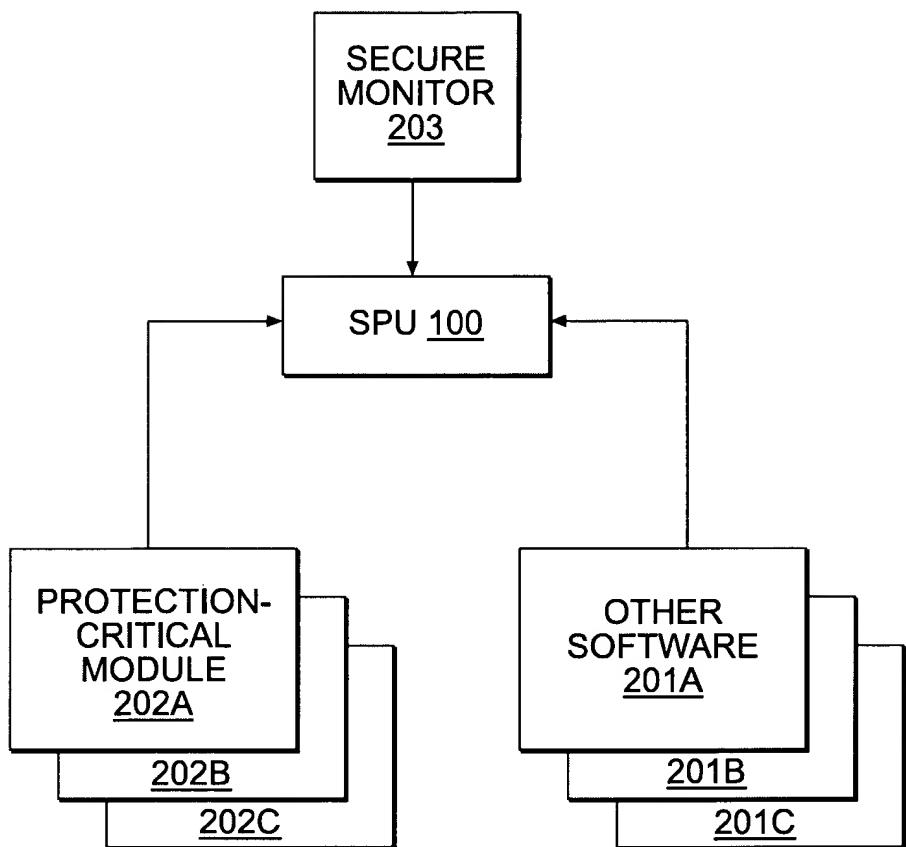


FIG. 3

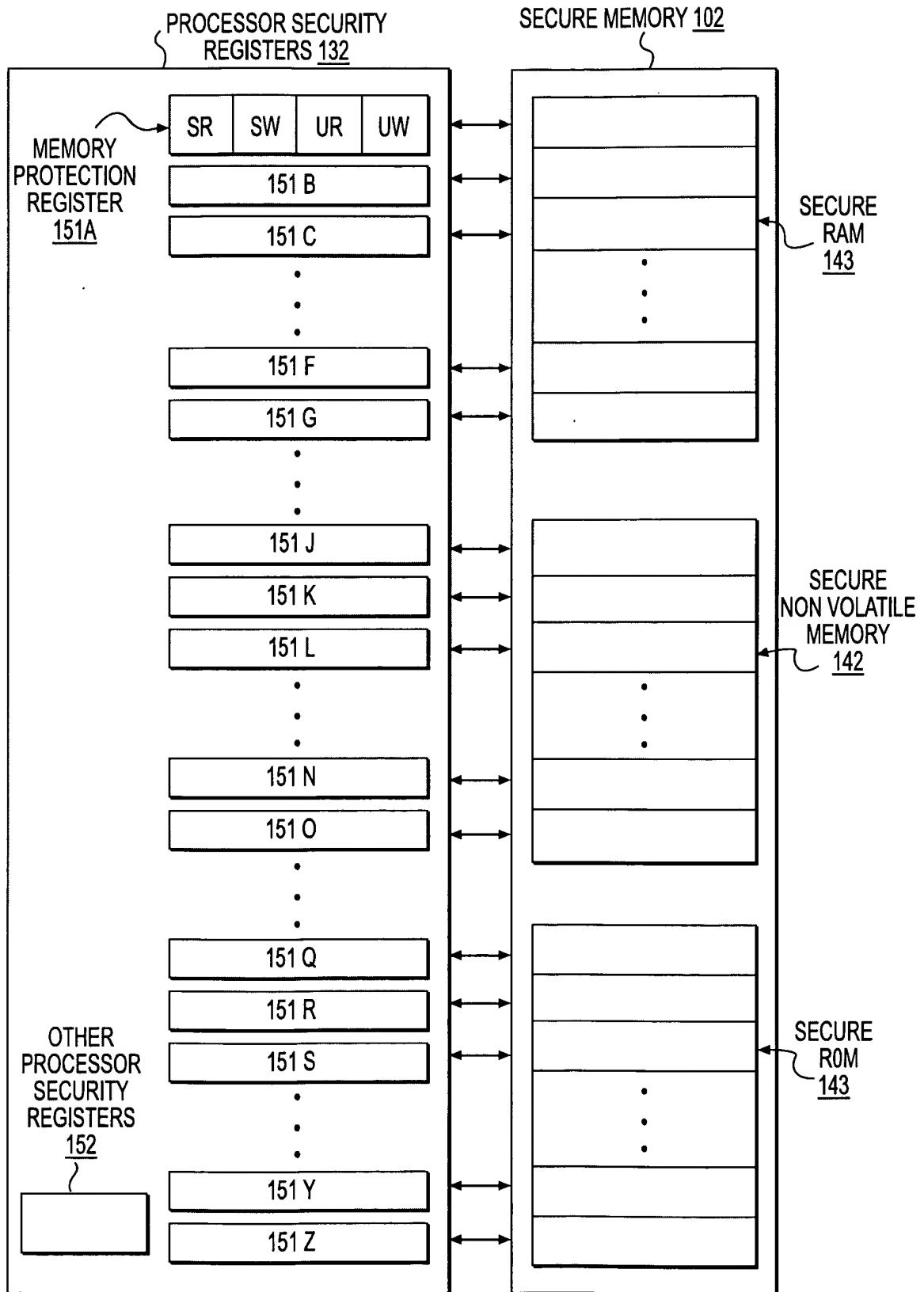


FIG. 4

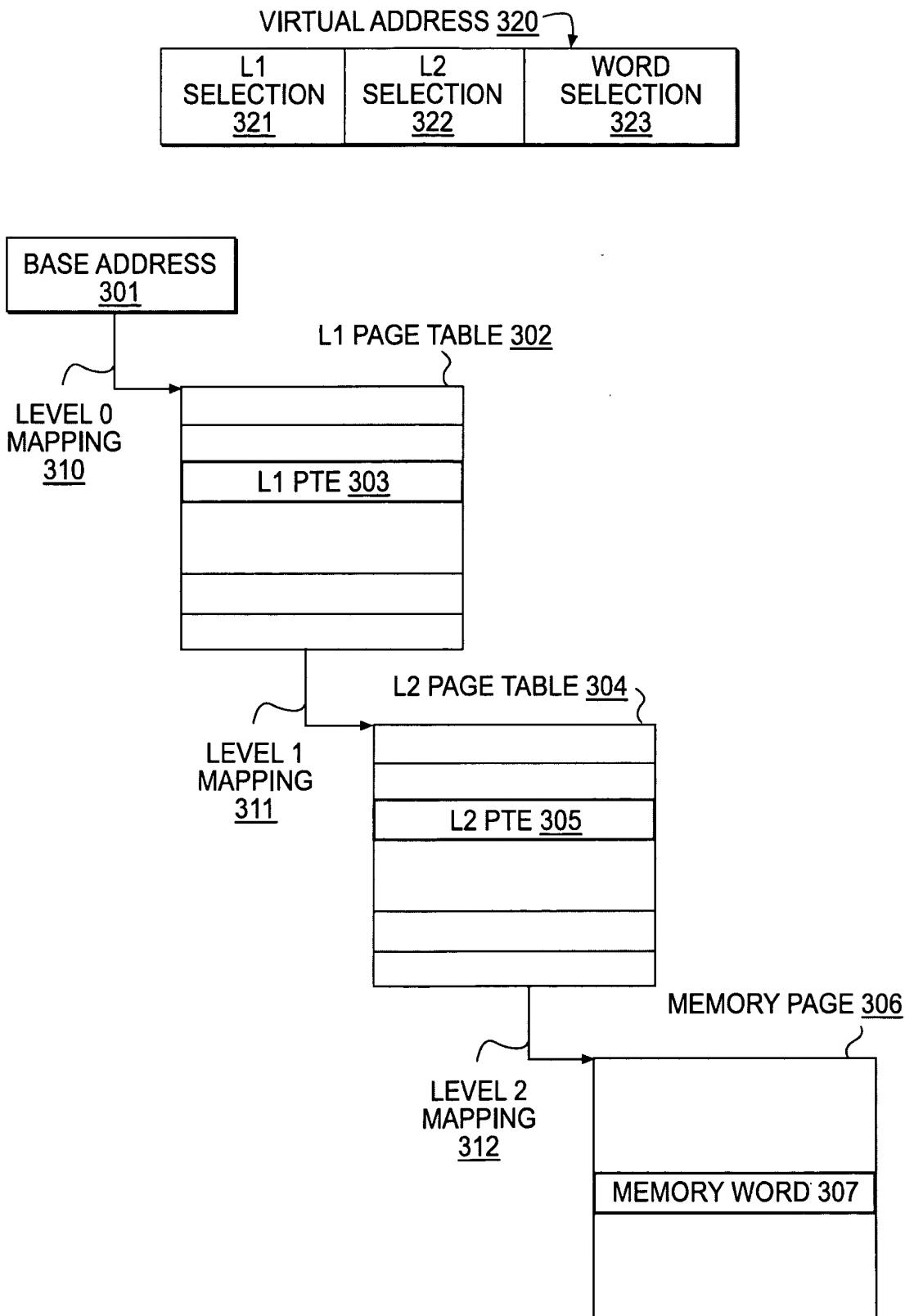


FIG. 5

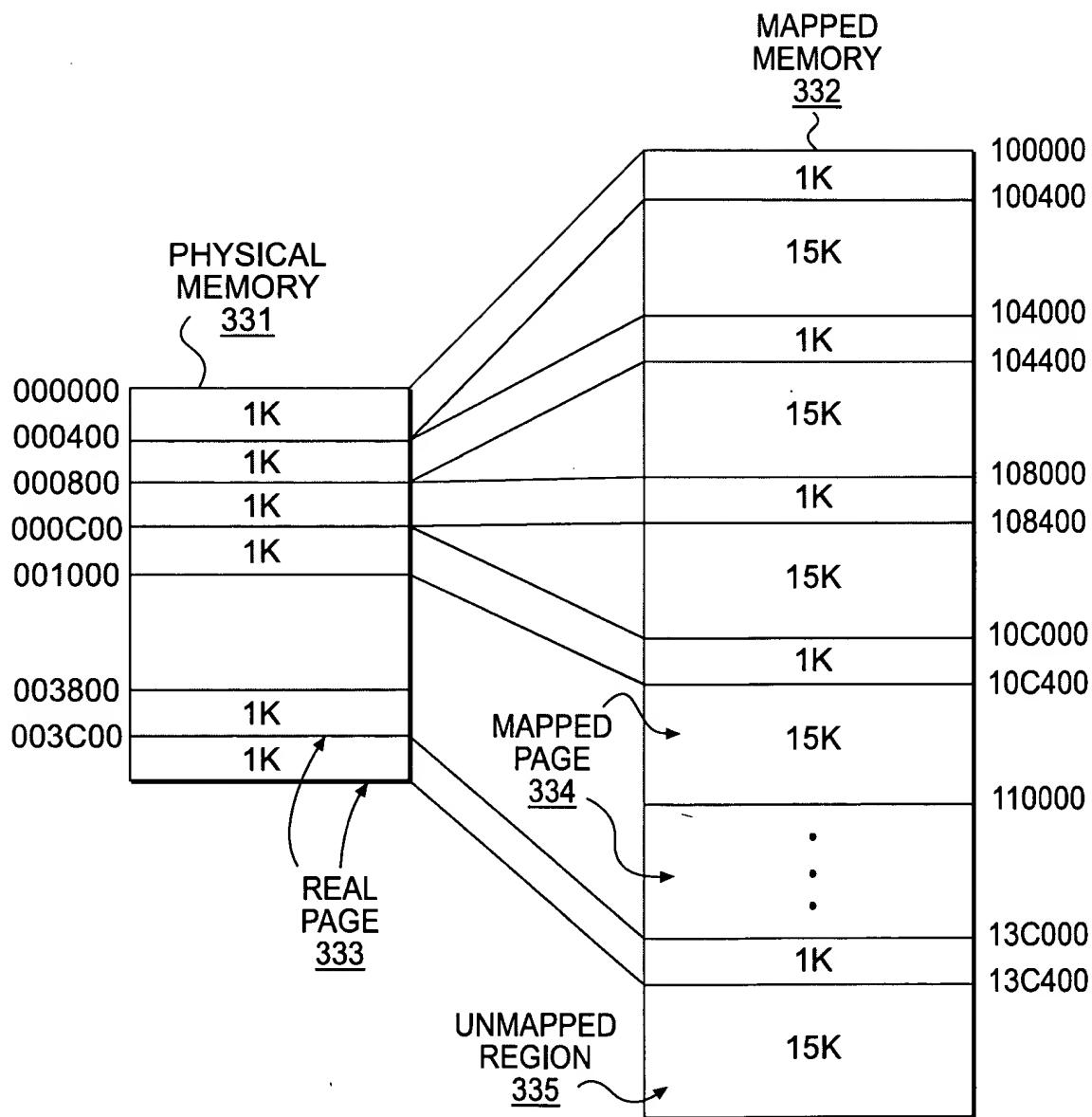


FIG. 6

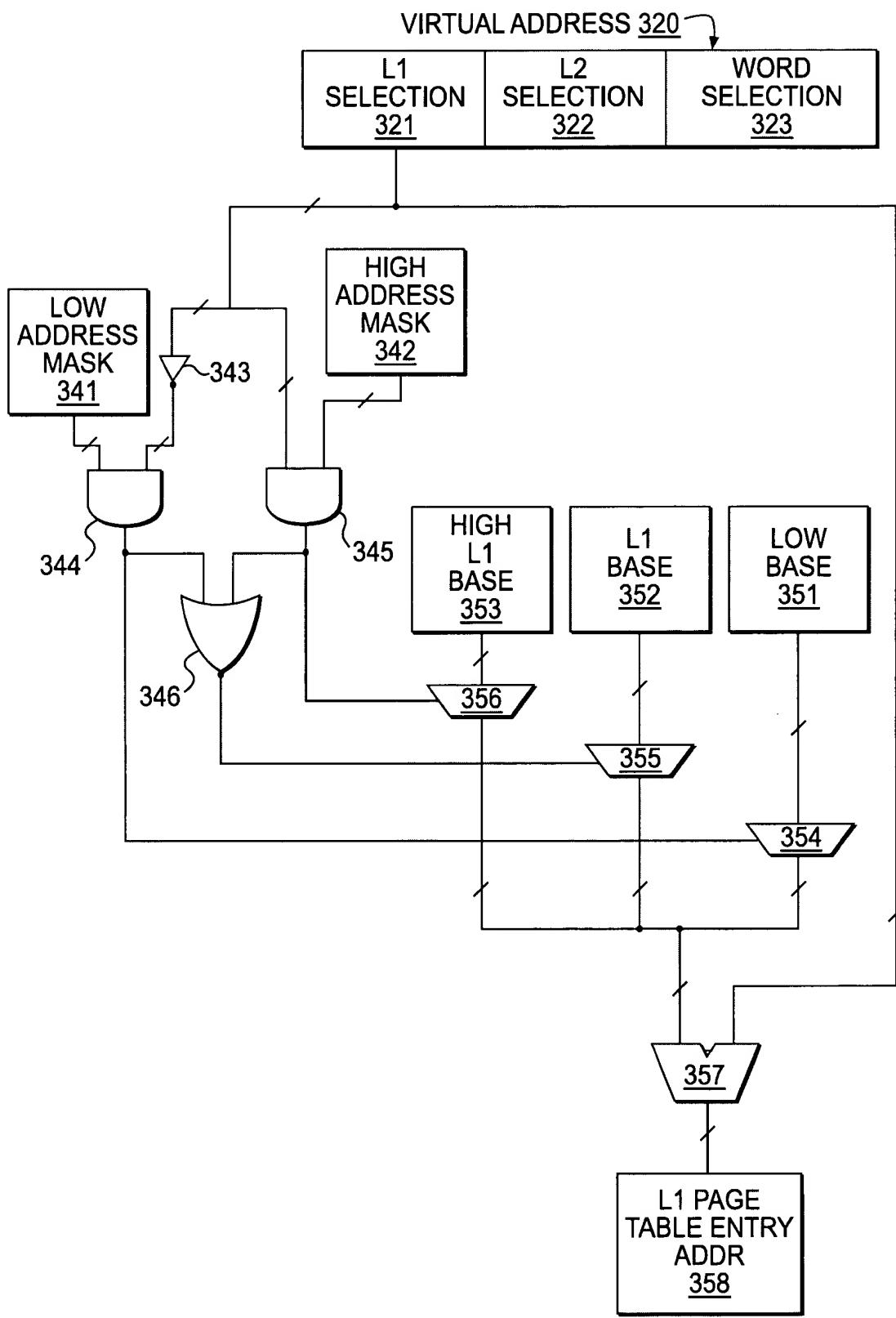


FIG. 7

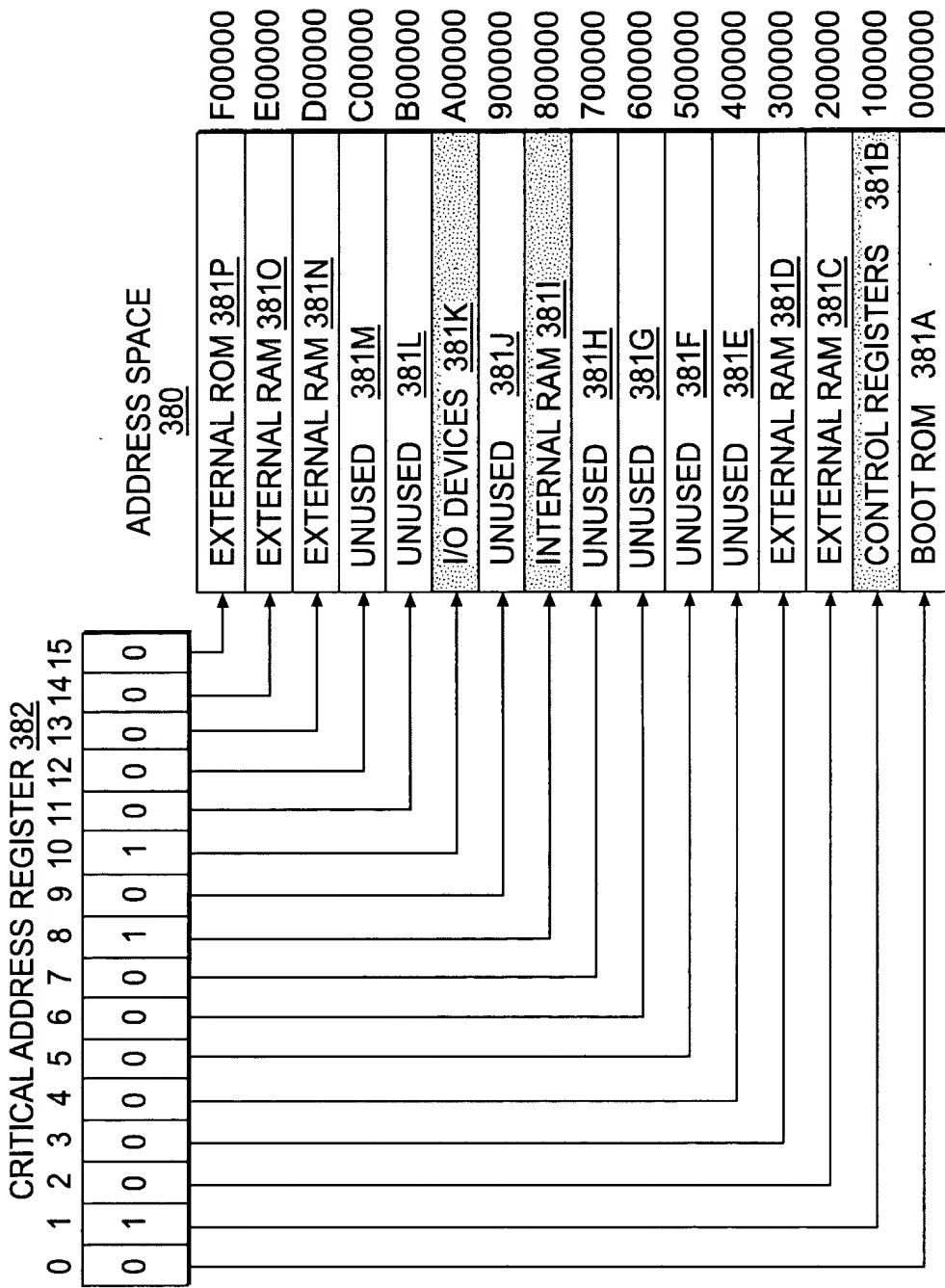


FIG. 8

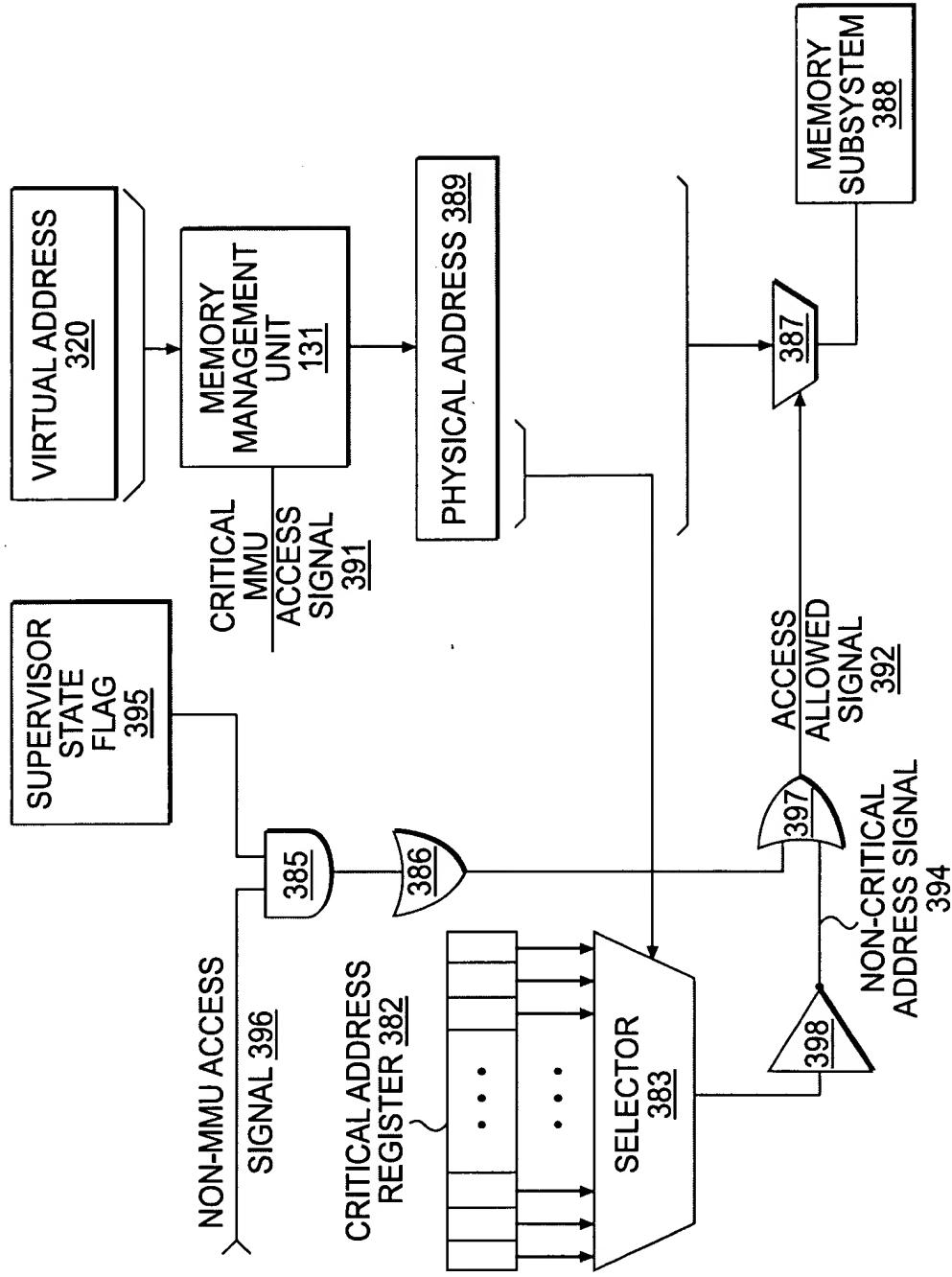


FIG. 9

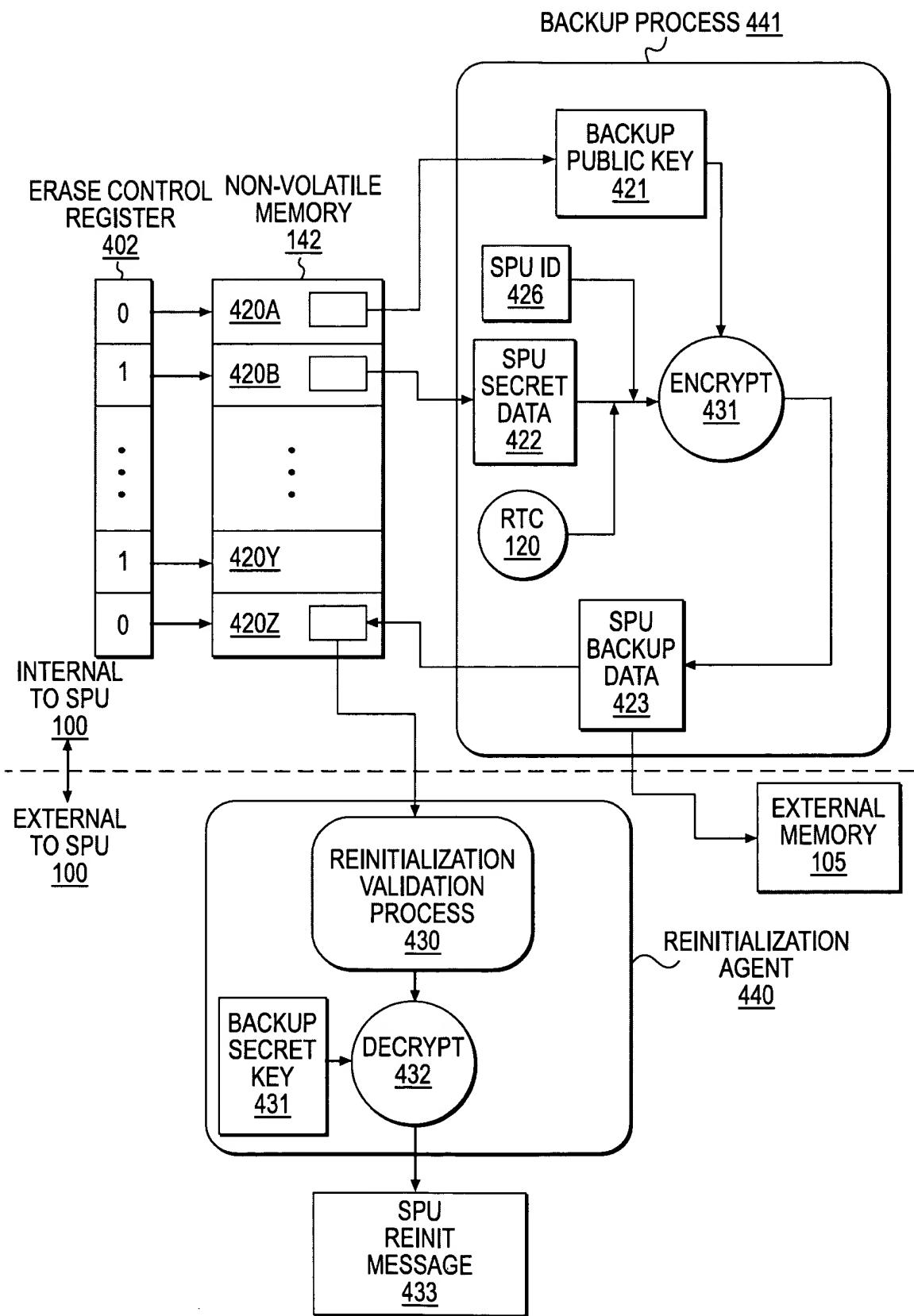


FIG. 10

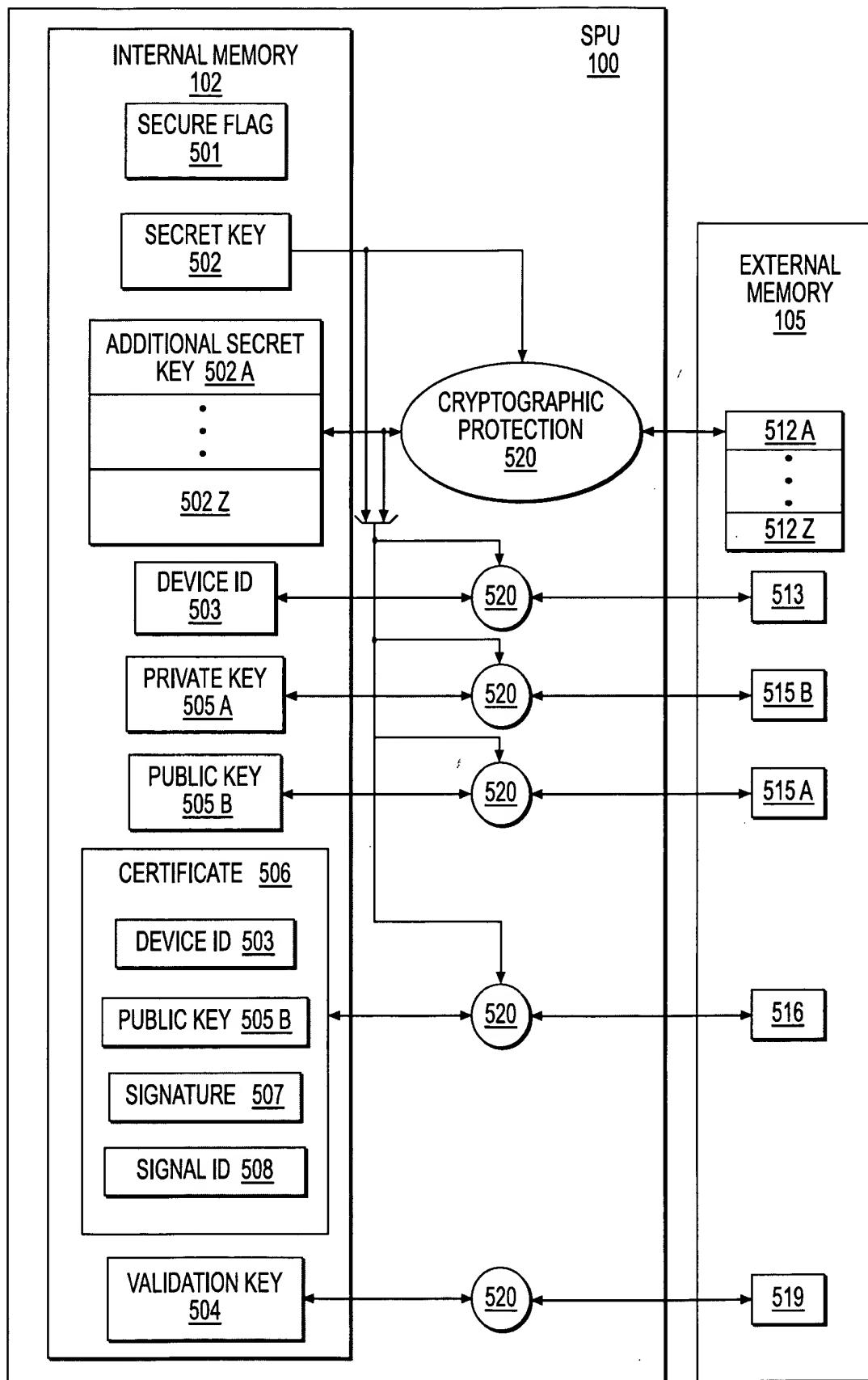


FIG. 11

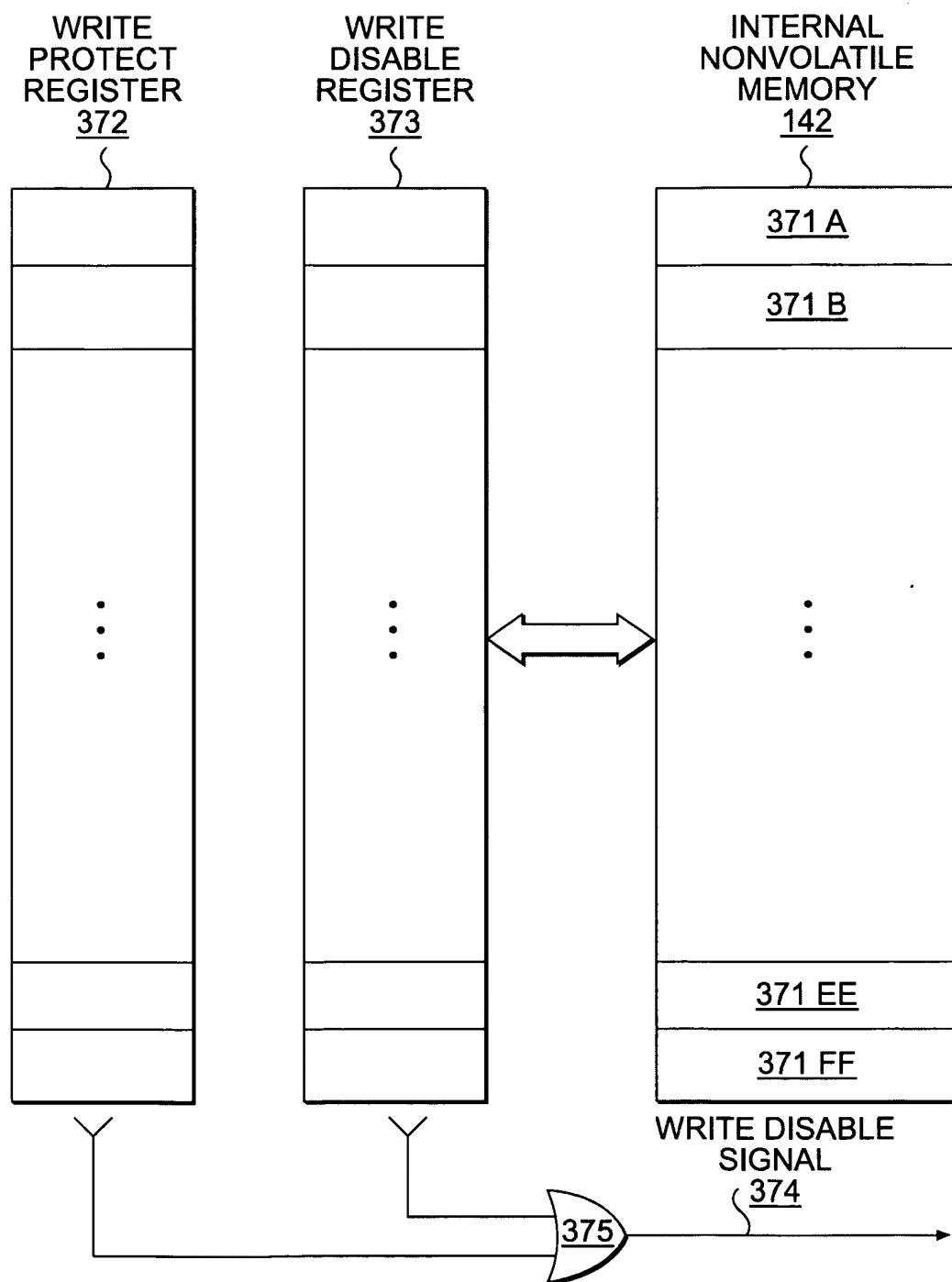
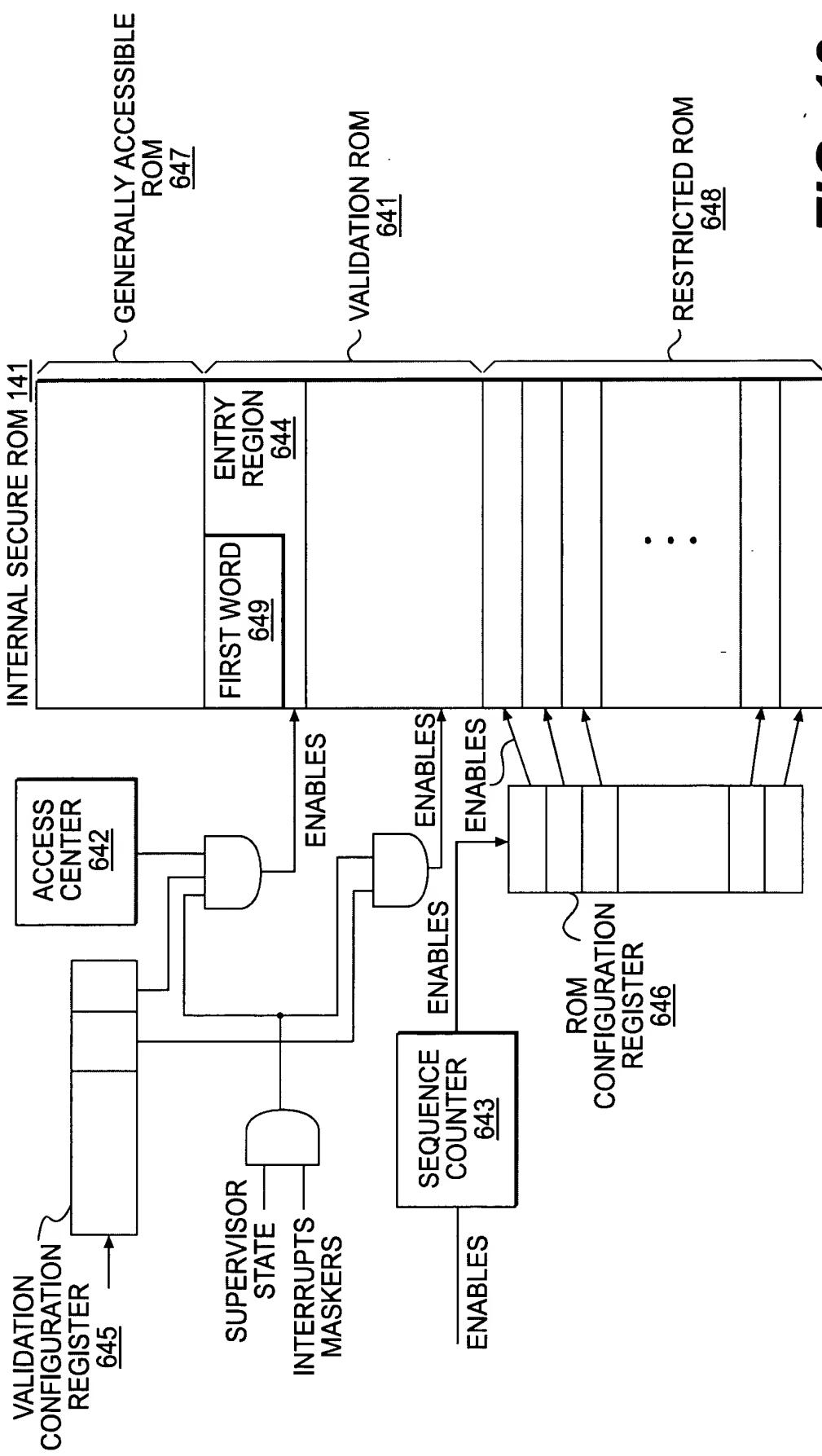
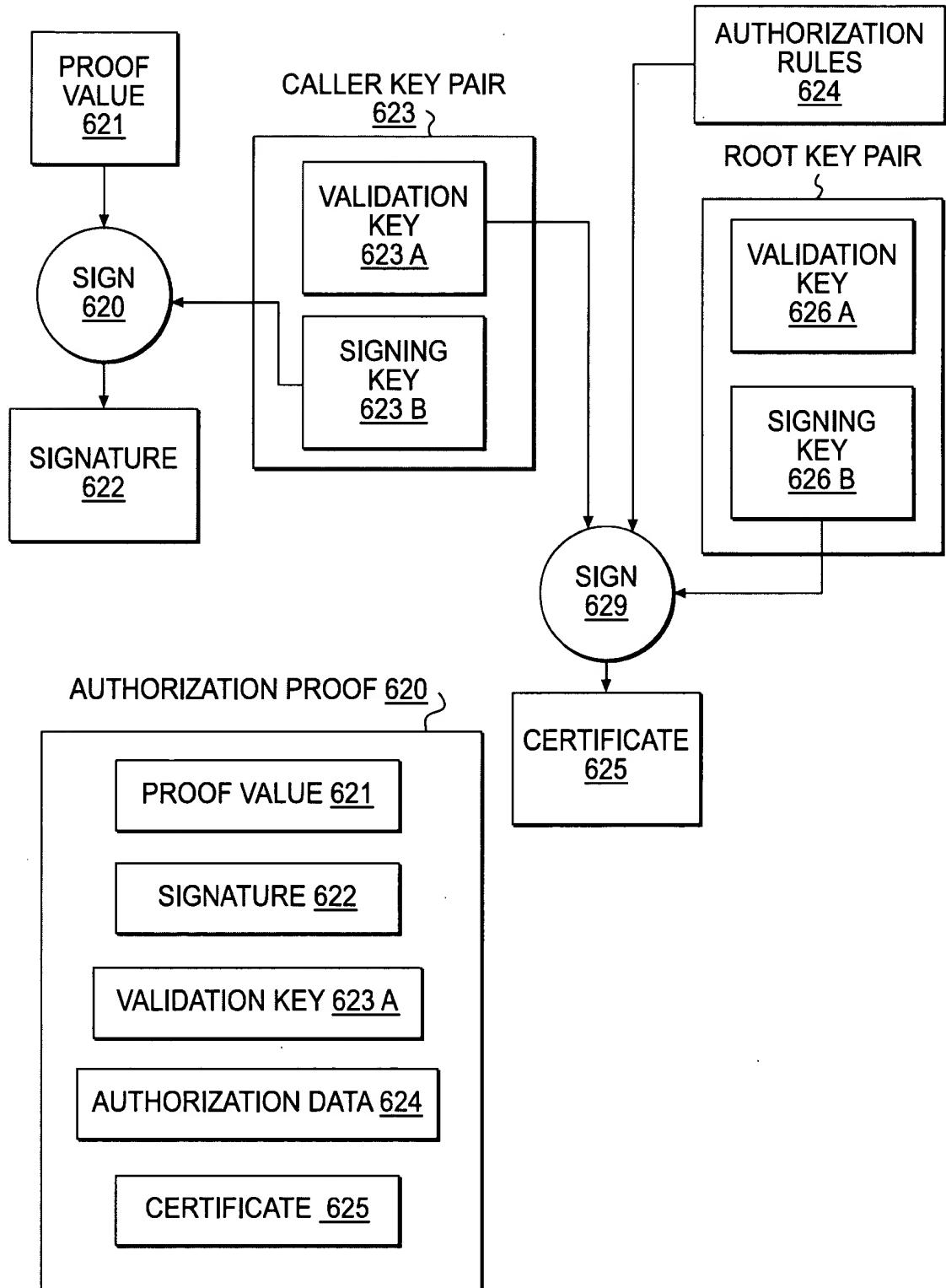


FIG. 12

FIG. 13

PROTECTED ROM PARTITIONING





AUTHORIZATION PROOF COMPONENTS

FIG. 14

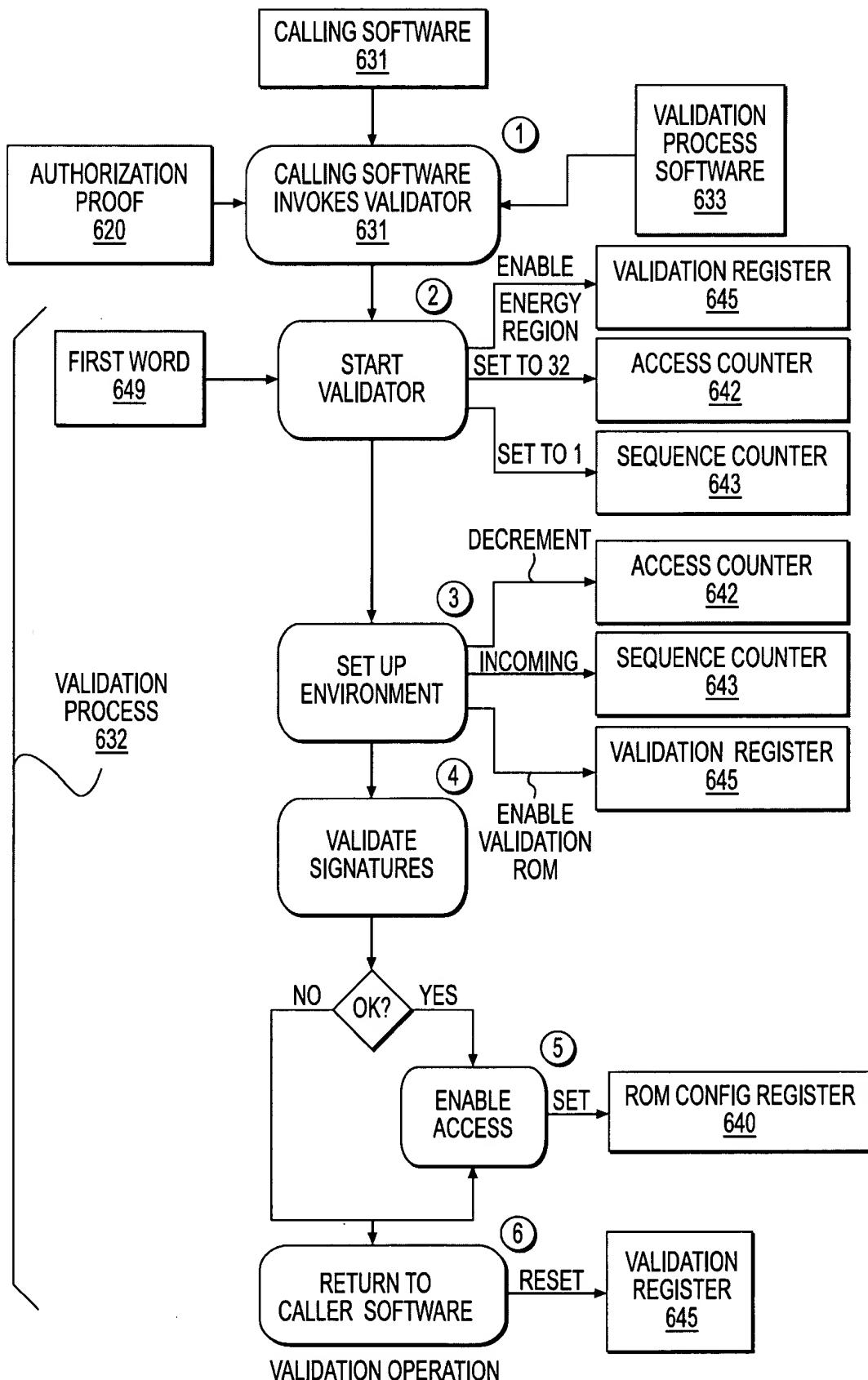
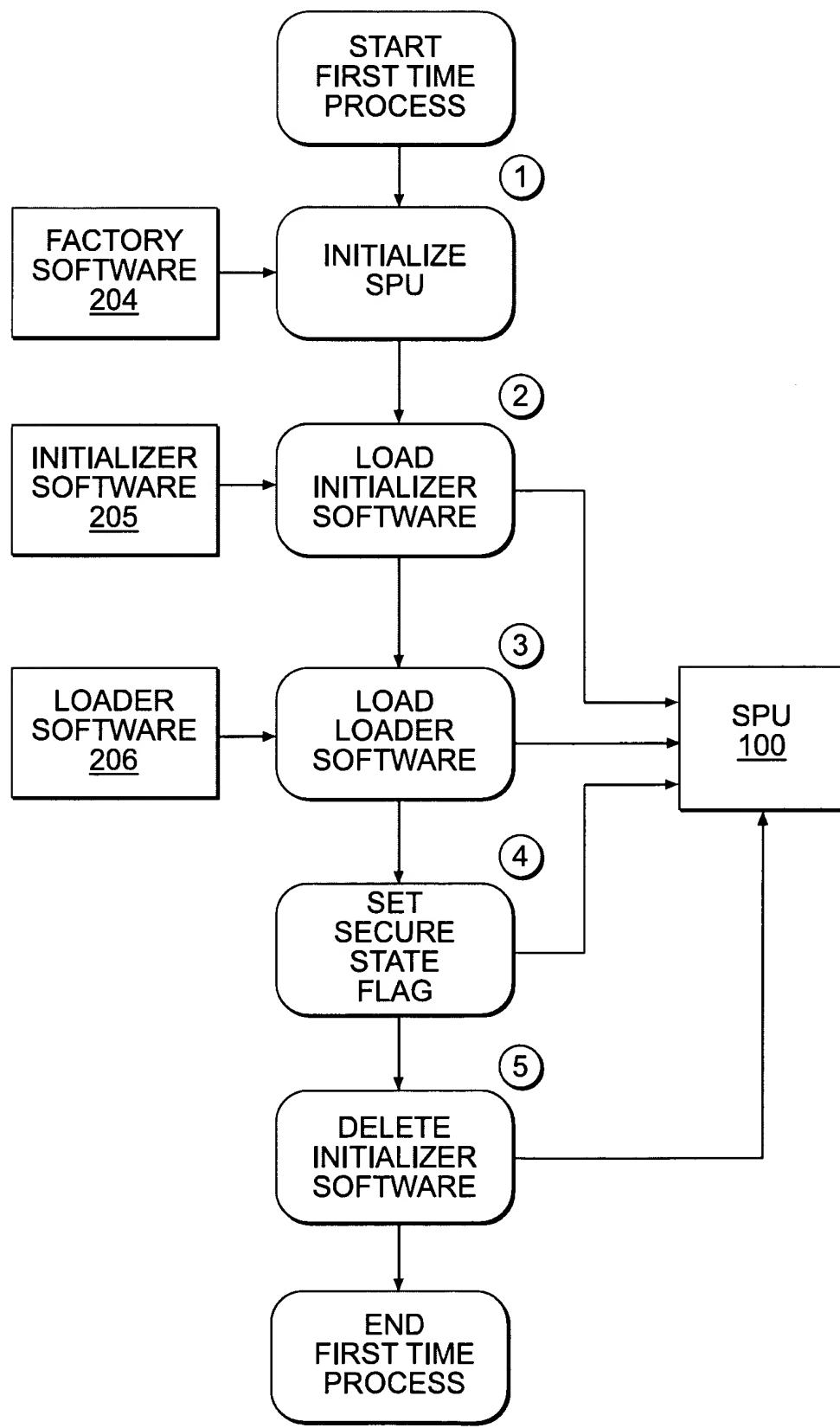
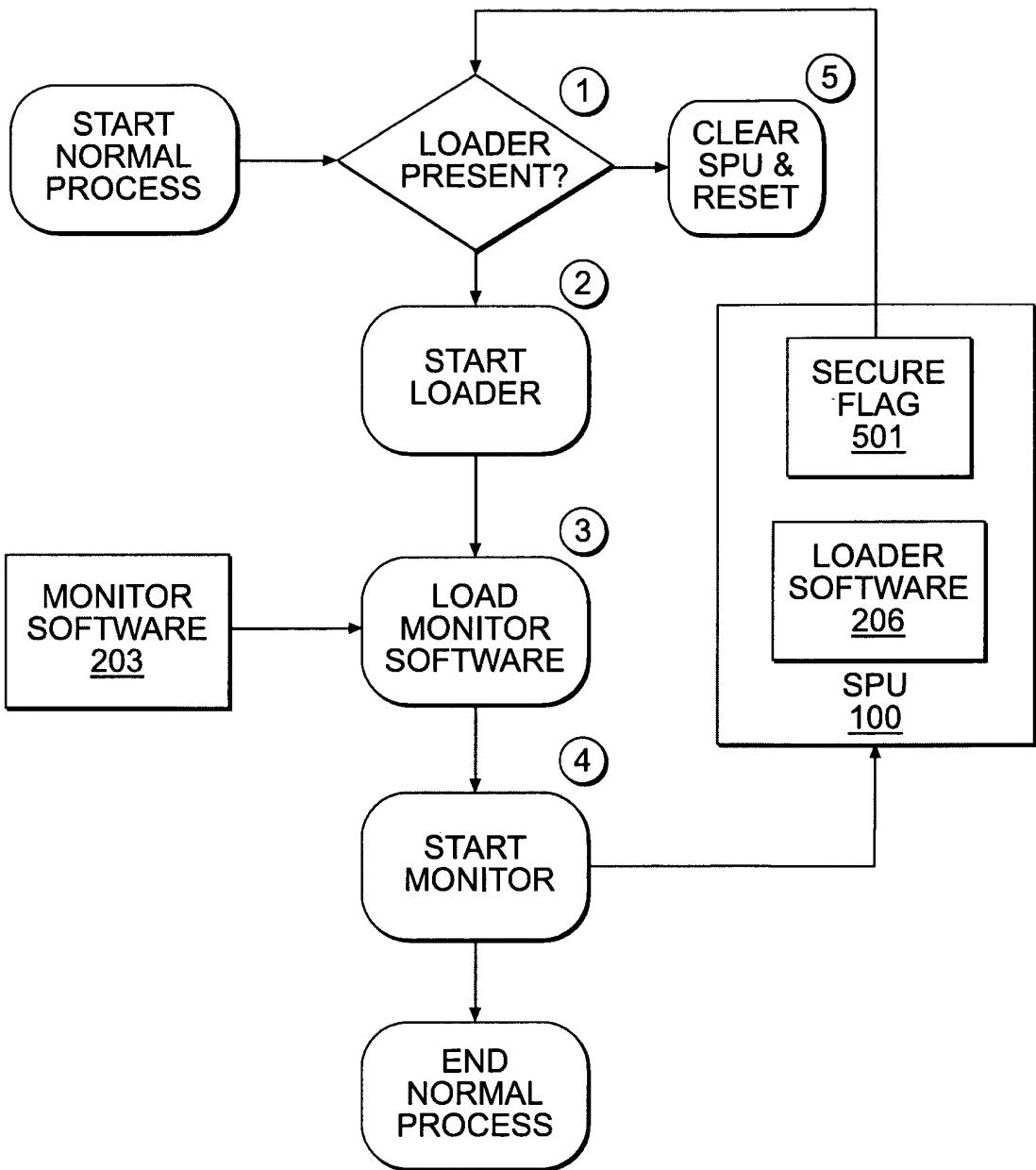


FIG. 15



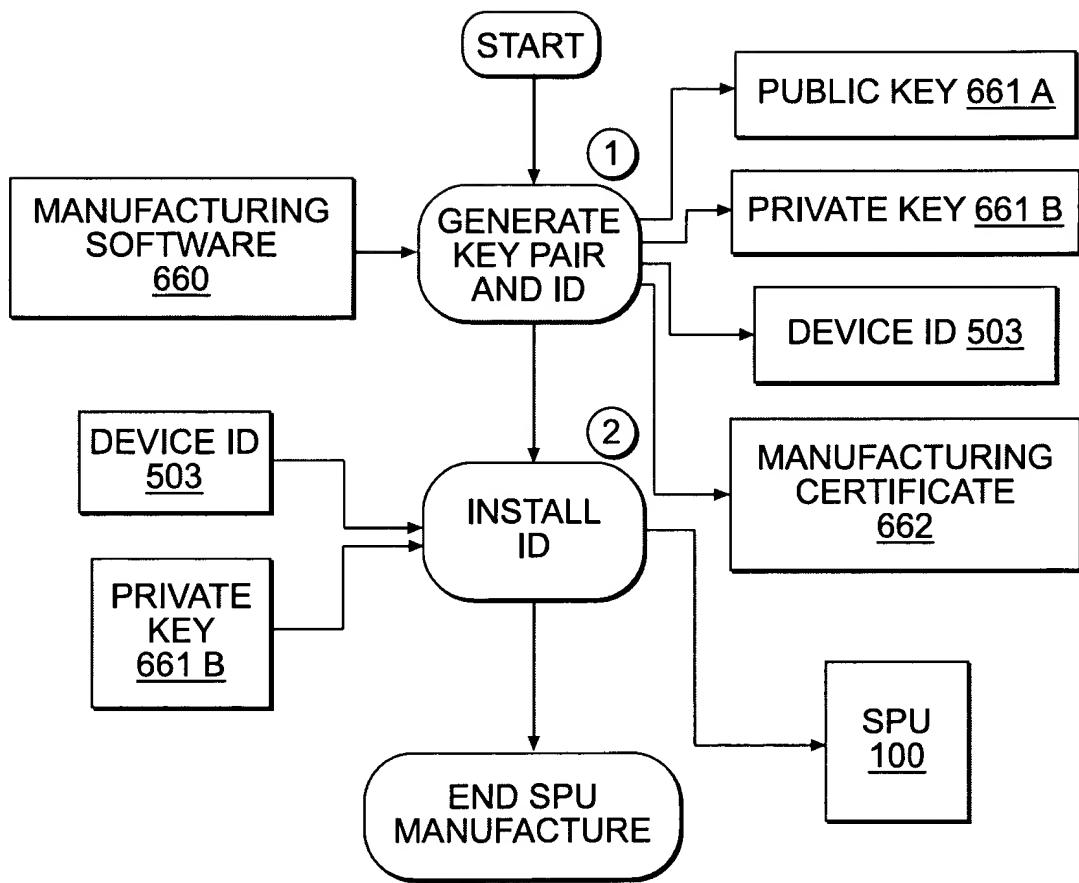
MONITOR SOFTWARE LOAD PROCESS (1)

FIG. 16



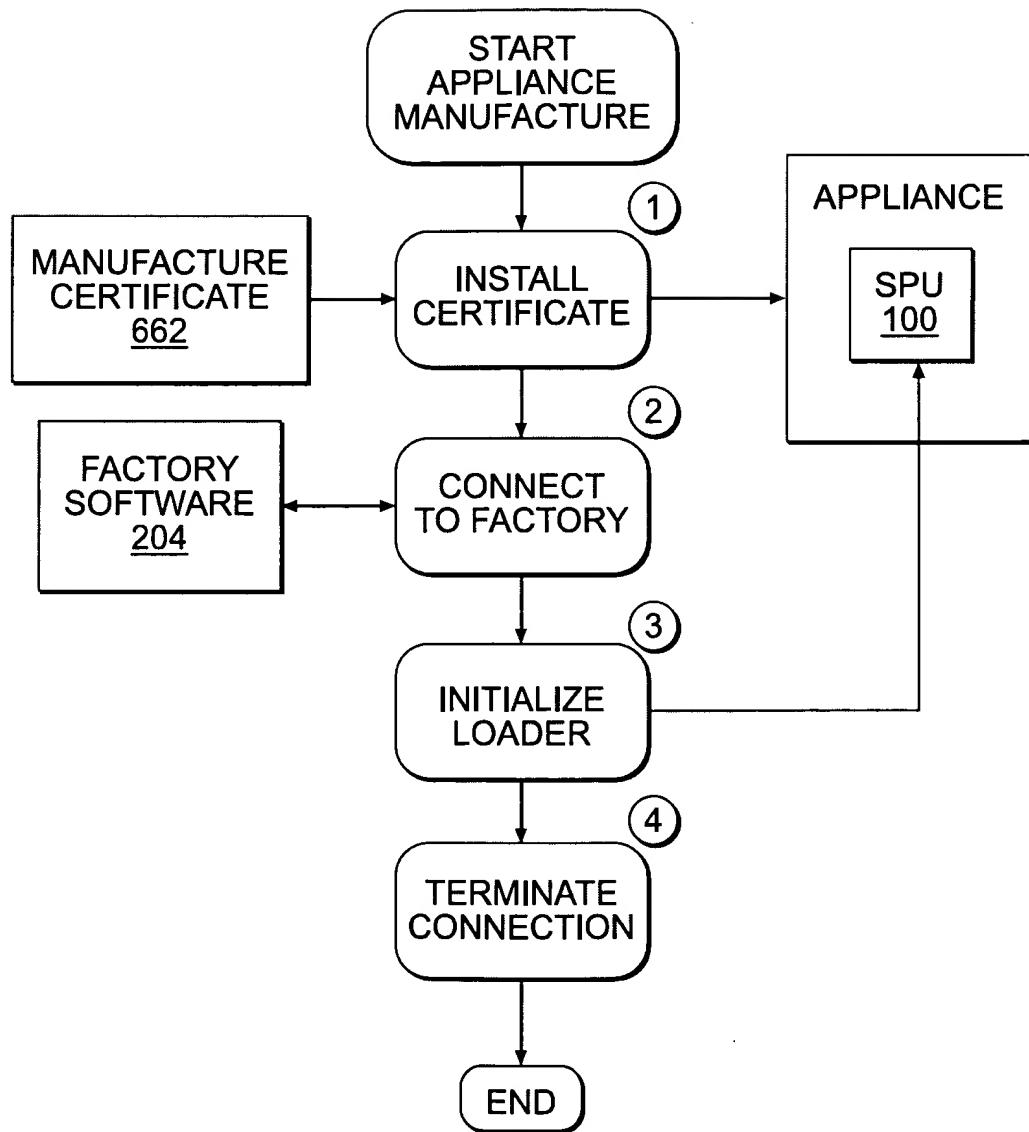
MONITOR SOFTWARE LOAD PROCESS (2)

FIG. 17



ALTERNATE LOAD PROCESS (1)

FIG. 18A



ALTERNATE LOAD PROCESS (2)

FIG. 18B